


All capacitor values are shown as farads.  
 All inductance values are shown as henries.  
 All resistor values are shown as ohms.

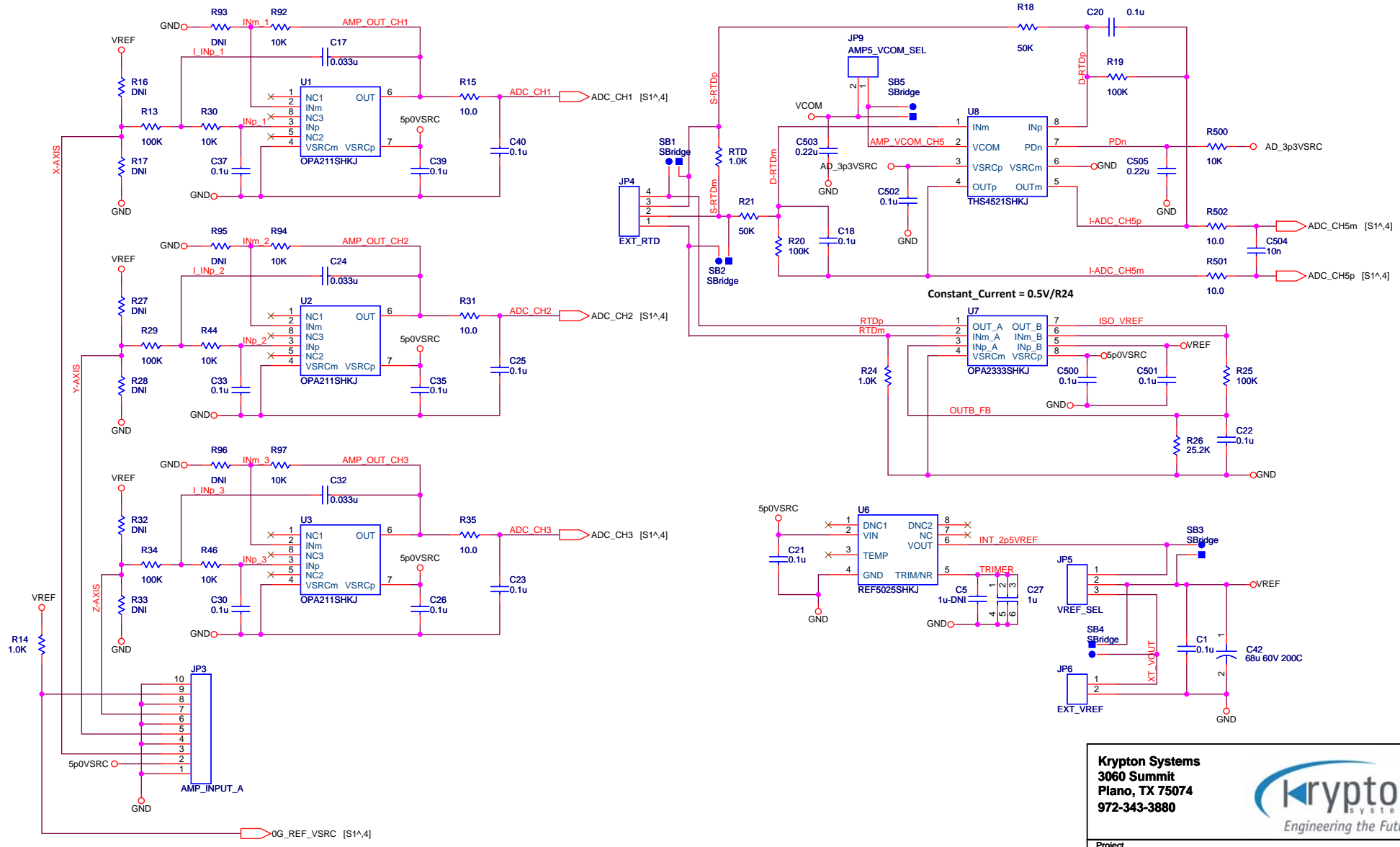
**Krypton Systems, LLC**  
 3060 Summit  
 Plano, TX 75074  
 972-424-3880



Project  
**TI HEAT EVM Rev-B**

Size <b>A</b>	Eng <b>KWH</b>	Drawing File Name <b>Hierarchical_Block_Diagram</b>	Rev <b>B</b>
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Date: **Friday, May 18, 2012** | Sheet **1** of **7**

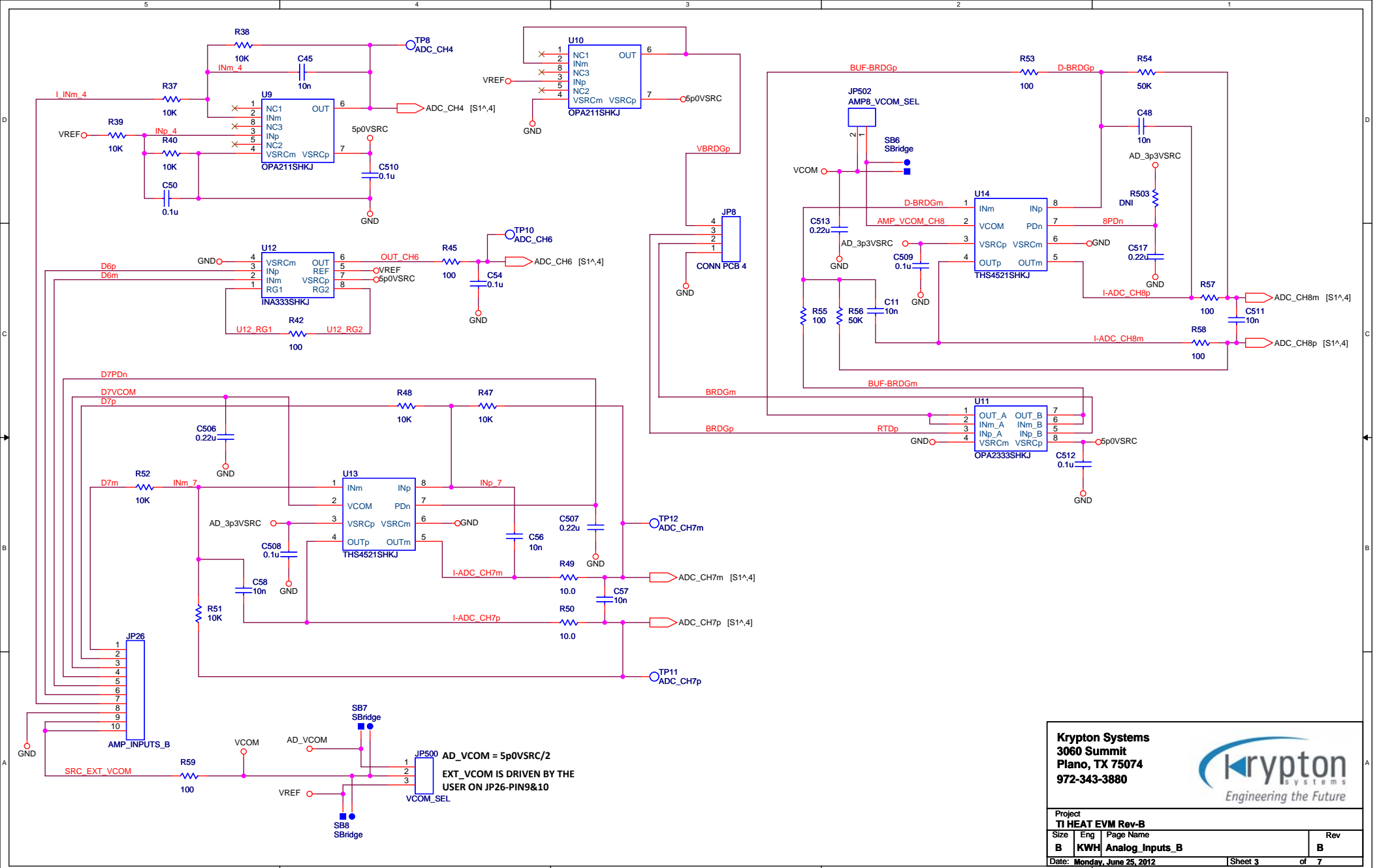


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 3060 Summit  
 Plano, TX 75074  
 972-343-3880

Project: TI HEAT EVM Rev-B

Size: B Eng: KWH Page Name: Analog\_inputs\_A Rev: B

Date: Monday, June 25, 2012 Sheet 2 of 7

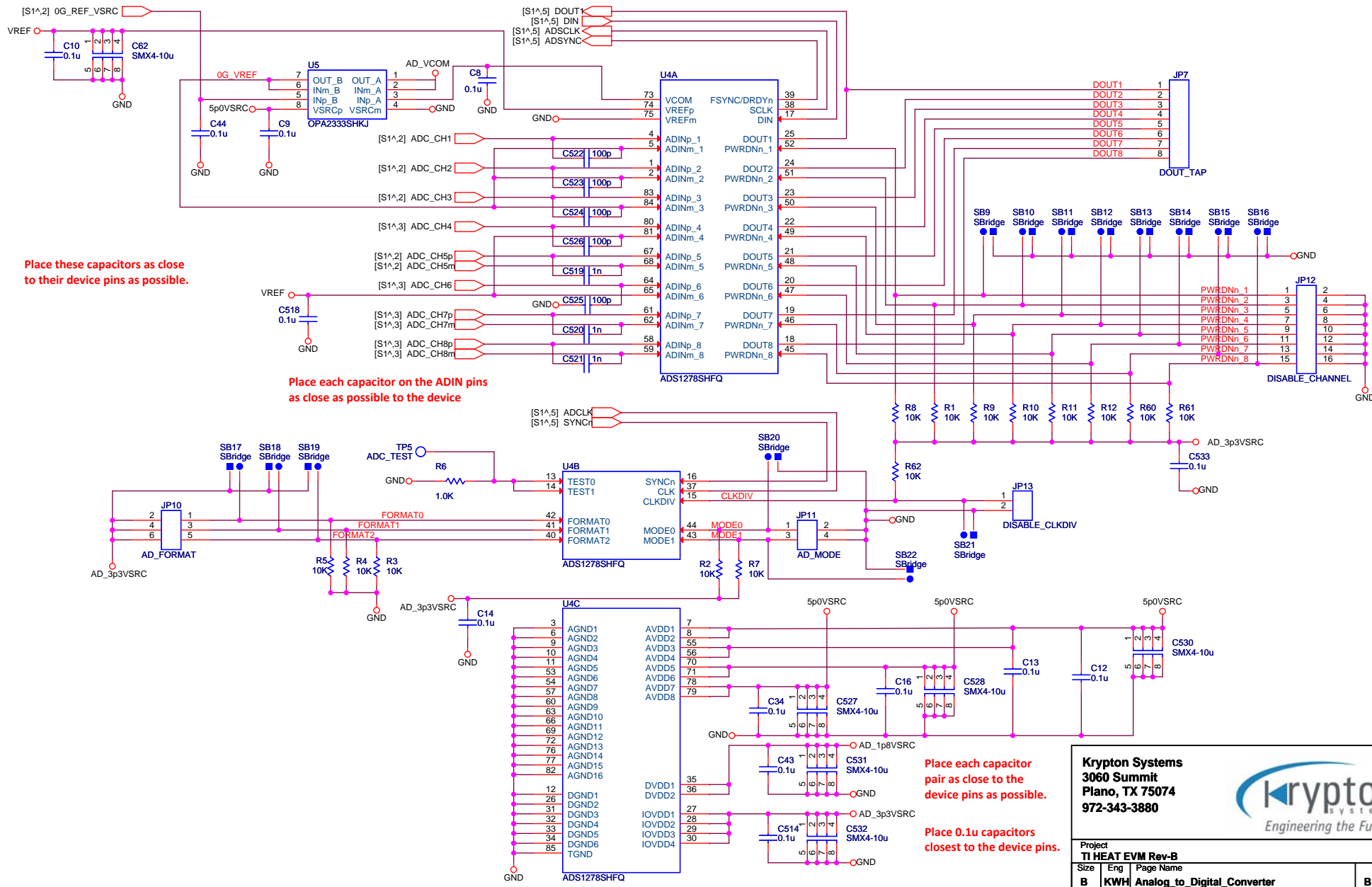


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 Plano, TX 75074  
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Project  
**TI HEAT EVM Rev-B**

Size	Eng	Page Name	Rev
B	KWH	Analog_inputs_B	B

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Place these capacitors as close to their device pins as possible.

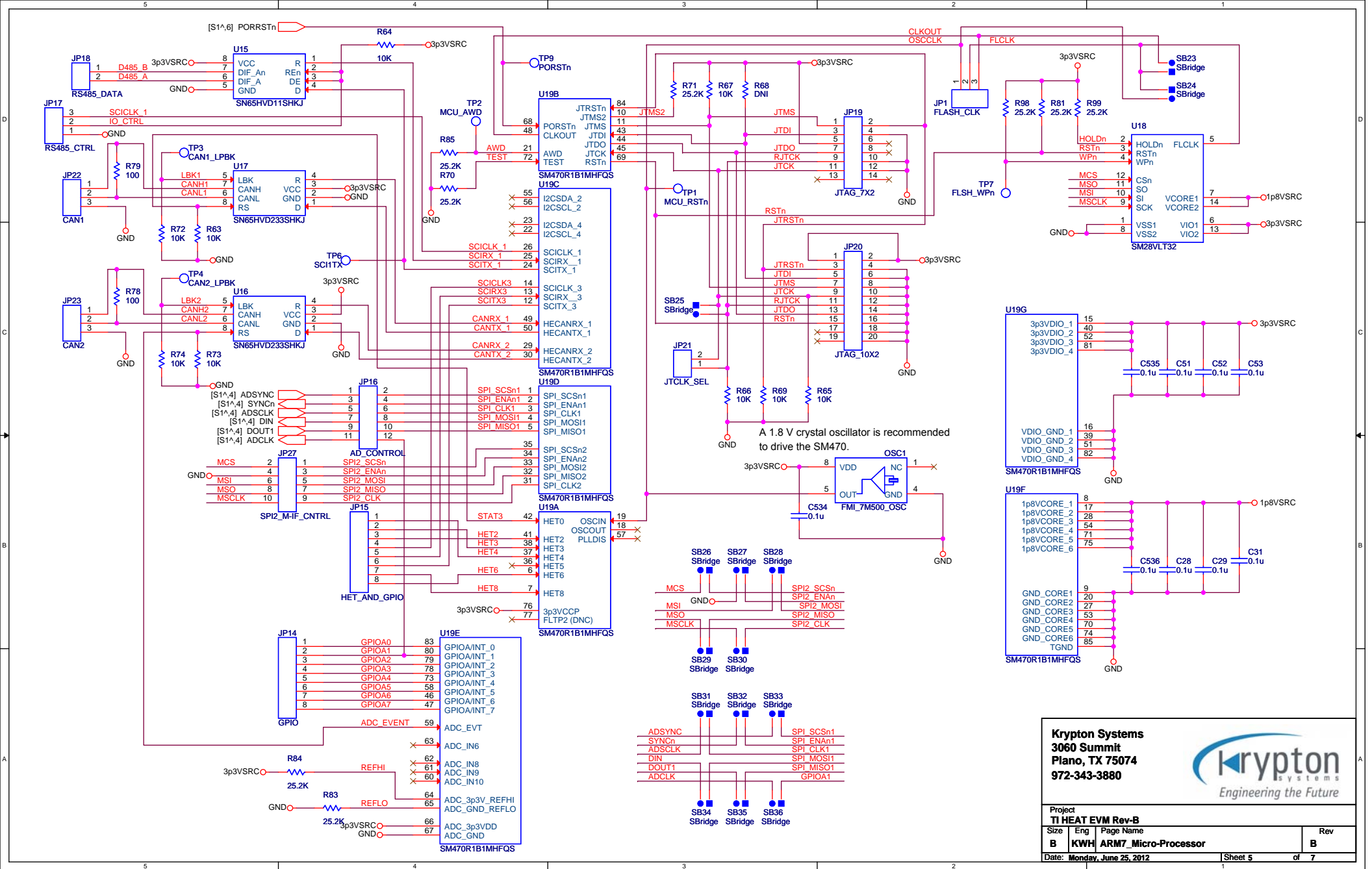
Place each capacitor on the ADIN pins as close as possible to the device

Place each capacitor pair as close to the device pins as possible.

Place 0.1u capacitors closest to the device pins.

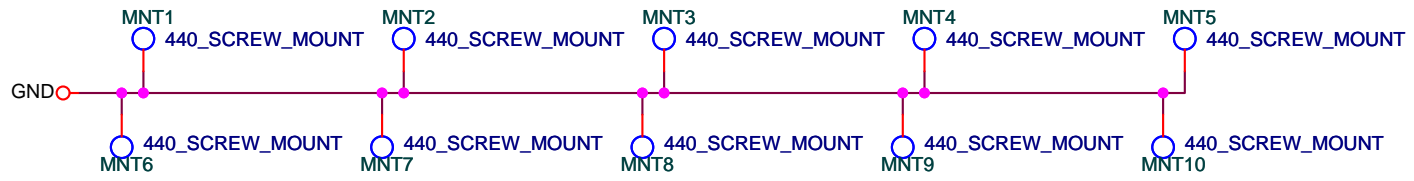
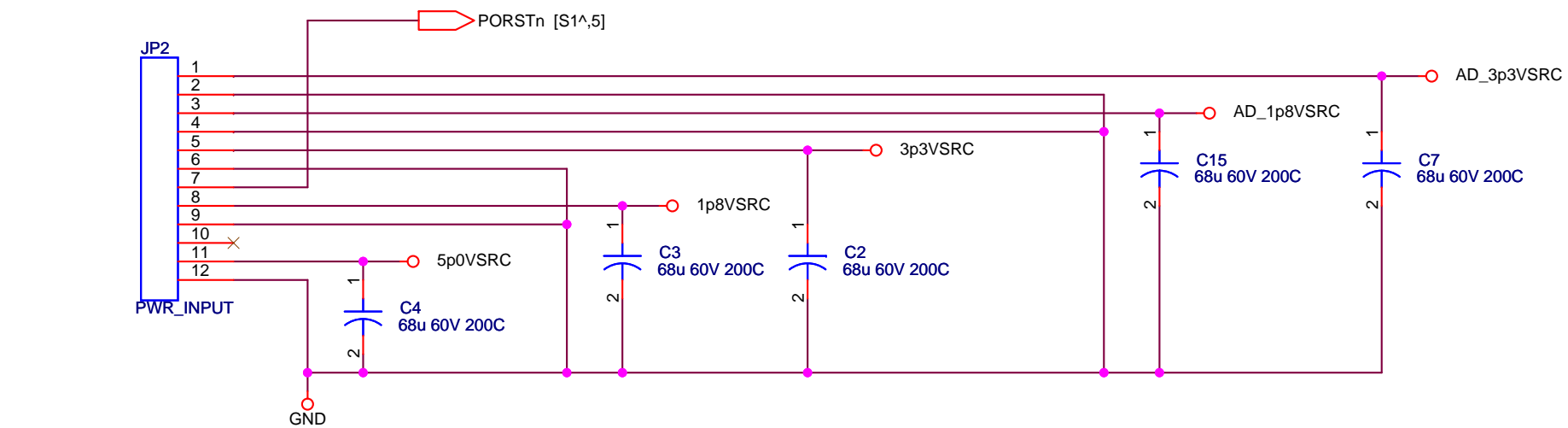
**Krypton Systems**  
 3060 Summit  
 Plano, TX 75074  
 972-343-3880

Project			
TI HEAT EVM Rev-B			
Size	Eng	Page Name	Rev
B	KWH	Analog_to_Digital_Converter	B
Date: Monday, June 25, 2012			Sheet 4 of 7



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**3060 Summit**  
**Plano, TX 75074**  
**972-343-3880**

Project: **TI HEAT EVM Rev-B**  
 Size: **B** Eng: **KWH** Page Name: **ARM7\_Micro-Processor** Rev: **B**  
 Date: **Monday, June 25, 2012** Sheet 5 of 7



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 3060 Summit  
 Plano, TX 75074  
 972-343-3880

Project  
**TI HEAT EVM Rev-B**

Size	Eng	Page Name	Rev
A	KWH	Power_Input_and_Decoupling	B

Date: Friday, May 18, 2012      Sheet 6 of 7

TI\_HEATEVM\_REV-M\_010312.DSN

FOLLOWING SCHEMATIC AND LAYER PLACEMENT REVIEW WE MODIFIED THE POWER INPUT STRUCTURES, CHANGED Y-AXIS AND Z-AXIS FILTERS SO THAT THEY EQUAL THE X-AXIS FILTERS. THE ITEMS IN REV-H, REV-J, AND REV-K WERE APPROVED.

**BOARD WAS RELEASED TO LAYOUT. LAYOUT REVIEW WILL BE HELD WHEN PLACEMENT IS FINALIZED.**

**KWH 01/18/12**

TI\_HEATEVM\_REV-N\_010312.DSN

MODIFIED SCHEMATIC TO REFLECT THE ERRORS NOTED DURING LAYOUT

- 1) SB27 HAS AN OPEN PIN
- 2) SB7 AND SB8 ARE CONNECTED TO THE SAME NET. SB8 WAS MOVED TO CORRECT NET
- 3) BOARD GREW 0.4 INCH LONGER AND NEEDED TWO MORE MOUNTING HOLES
- 4) ADDED C535 AND C536 TO ARM7 SO THAT EACH SIDE OF THE DEVICE HAS A DECOUPLING CAP ON BOTH POWER NETS

**KWH 01/23/12**

TI\_HEATEVM\_REV-P\_010312.DSN

ADDED FOOTPRINT TO JP2

**KWH 01/23/12**

TI\_HEATEVM\_REV-S\_010312.DSN

THE REFHI NET WAS BROKEN, IT WAS CONNECTED AT R84.

**KWH 01/27/12**

TI\_HEATEVM\_REV-T\_010312.DSN

SOME COMPONENT VALUES WERE UPDATED PER THE NEW DESIGN RULES FROM TI. SOME PART NUMBERS AND/OR FOOTPRINTS WERE UPDATED TO BRING BOM IN STEP WITH THE OLD BOM>

**KWH 02/01/12**

TI\_HEATEVM\_REV\_A1\_030912.DSN

REVIEW WITH HECTOR TORRES. SCHEMATIC EDITED BASED ON HECTORS FEEDBACK. A/D CONNECTIONS TO 0G NET, VCOM AND VREF NETS WERE MODIFIED

**NIMA 03/09/12**

**Krypton Systems**  
3060 Summit  
Plano, TX 75074  
972-343-3880



Project  
**TI HEAT EVM Rev-B**

Size	Eng	Page Name	Rev
<b>A</b>	<b>KWH</b>	<b>ECN_and_Change Requests</b>	<b>B</b>

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